

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	(external with test\$3 with data with scan\$3 with (integrated adj circuit) with (return\$3 or output\$4) with result\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:47
L3	0	(low with voltage with below with time with period with timeout)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:48
L4	0	(low with voltage with below with time with period) same timeout	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:48
L5	12	(low with voltage with below with time) same timeout	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:50
L7	0	(low with voltage with timeout) with (integrated adj circuit) with pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:51
L8	0	(low with voltage with timeout) with (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:51
L9	0	(low with voltage with timeout) same (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:52
L10	22	(low with voltage with timeout) and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:56

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L11	0	(low with voltage with timeout) and ((integrated adj circuit) with pin)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/30 23:52
L12	2	(below with low with voltage with timeout) and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/01 00:01
L13	9	(below with low with voltage with timeout)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/01 01:30
L15	0	(714/724.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:34
L16	0	(714/738.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:34
L17	0	(714/715.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:34
L18	0	(714/25.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:34
L19	0	(714/27.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:35
L20	0	(714/726.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:35

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L21	0	(714/727.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:36
L22	0	(714/729.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:36
L23	0	(714/730.ccls.) and (integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:36
L24	7	(integrated adj circuit) and ((input adj output) with pin with single) and (external with test\$3 with circuit\$2) and ((logic or voltage\$1) with threshold\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/01 01:37
L25	1	winter-rudi-de.in.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/10/01 01:40

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Edit an existing query or compose a new query in the Search Query Display.

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#1 (integrated circuit<and>single input output pin)<and>external test circuitry

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John Colter, Netscape Navigator

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